

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Douglas G. Wildes et al. : Group Art Unit:

Serial No.: To Be Assigned : Examiner:

Filed: Concurrently

Title: HIGH-DENSITY CONNECTION BETWEEN
MULTIPLE CIRCUIT BOARDS

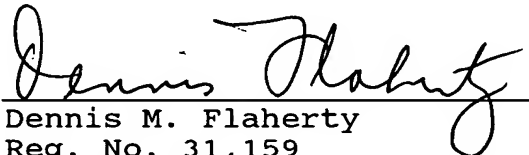
Hon. Commissioner of Patents & Trademarks
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 CFR § 1.97, the Applicants hereby request that the references cited in the accompanying Form PTO-1449 be considered by the examiner in the above-entitled application and made of record therein. Copies of the cited references are submitted herewith.

Respectfully submitted,



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March 30, 2004
Date

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUB- CLASS	FILING DATE
	A	5,160,269	11/3/92	Fox, Jr. et al.	439	67	
	B	6,007,490	12/28/99	Pawluskiewicz	600	459	
	C	6,017,244	1/25/00	Daane	439	495	

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATIONS

EXAM- INER INITIAL		PUB. NUMBER	PUBLI- CATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUB- CLASS	TRANS- LATION	
							YES	NO

OTHER DOCUMENTS

(Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner:	Date Considered:
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.